

**Amendments to the Specification:**

Please replace paragraph beginning at page 1, line 10, with the following amended paragraph:

This patent application hereby incorporates by reference US patent application entitled "METHOD AND APPARATUS FOR OPTIMIZING BODY BIAS CONNECTIONS IN CMOS CIRCUITS USING A DEEP N-WELL GRID STRUCTURE," filed on [[\_\_TBD\_\_]] October 10, 2003, serial number [[\_\_TBD\_\_]] 10/683,961, by [[\_\_\_\_\_]] Burr et al., attorney docket number TRAN-P143 and assigned to the assignee of the present invention.